



**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

**Hashemi, et. al.**

Serial No.: 09/878,815

Filed: June 11, 2001

For: **Structure and Method for Fabrication of  
a Leadless Chip Carrier**

Art Unit: 2811

Examiner: Patricia M. Costanzo

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**FIRST SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

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Sir:

In this Information Disclosure Statement and pursuant to 37 C.F.R. §§ 1.56 and 1.97, Applicants hereby disclose to the Patent Office patents, publications or other information of which Applicants are aware. A list of U.S. patent numbers is included in Form 1449 submitted herewith.

The items identified in this Information Disclosure Statement may or may not be “material” as defined in 37 C.F.R. § 1.56, and the submission thereof by Applicants is not to be construed as an admission that any such patent, publication or other information referred to is material or considered to be material (37 C.F.R. § 1.97(h)), or even qualifies as “prior art” under 35 U.S.C. § 102 with respect to the present invention unless specifically designated by Applicants as such. Identification of any reference or patent herein is not an admission, nor is it to be construed as an admission, that it was invented prior to the invention disclosed herein.

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Respectfully submitted,  
Farjani & Farjani LLP

Dated: 6/6/02

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